



# KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేయోగికీ एवं विज्ञान संस्थान, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత  
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2022-23

### I SEMESTER of M. Tech (CESP)

Class Teacher : D. Santhosh Kumar

Day	9.10-10.00	10.00-10.50	10.50-11.40	11.40-12.30	LUNCH BREAK	13.30-14.20	14.20-15.10	15.10-16.00	16.00-16.50
Monday	Advanced Communication Theory Lab			DSPPA		RMIPR B-VI-101	ASP	--	--
Tuesday	ACT	ASP(T)	WSN	DSPPA(T)		Advanced DSP Processors Lab			--
Wednesday	Advanced Communication Theory Lab			ERPW (T) SH 3-5		WSN(T)	ACT		--
Thursday	ERPW (T) SH 3-5	ACT	DSPPA	ASP		RMIPR B-VI-103	WSN		
Friday	ASP	WSN	DSPPA	ACT(T)		Advanced DSP Processors Lab			
Saturday									

Course Title	Course Code & Course Name	Course Faculty
ACT	P20SP101: Advanced Communication Theory	Mr. V. Shoban Reddy
DSPPA	P20SP102: DSP Processors and Architectures	Mr. D. Santhosh Kumar
WSN	P20SP103A: Wireless Sensor Networks	Dr. S. Siva Priyanka
ASP	P20SP104A: Adaptive Signal Processing	Dr. D. Venu
RMIPR	P20MC107 Research Methodology & IPR	Dr. R. Srikanth
ERPW	P20AC108A: English for Research Paper Writing	Dr.M. Om Prakash
ACT LAB	P20SP105: Advanced Communication Theory Lab	Mr. V. Shoban Reddy
ADSPP LAB	P20SP106: Advanced DSP Processors Lab	Mr. D. Santhosh Kumar

Sd/-  
Faculty I/c,  
Dept. Timetables

Sd/-  
Faculty I/c,  
Overall Timetables

Sd/-  
Head of the  
Department

Sd/-  
Dean, AA

Sd/-  
Principal